	Application No.	Applicant(s)	
Notice of Allowability	10/679,179	WANG ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Ly D. Pham	2827	
The MAILING DATE of this communication appears All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not include will be mailed in due	ed course. <b>THIS</b>
1. This communication is responsive to <u>03 October 2003</u> .			
2. The allowed claim(s) is/are <u>1-20</u> .			•
3. $\boxtimes$ The drawings filed on <u>03 October 2003</u> are accepted by th	e Examiner.		
4.			
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendr 8. Examiner's Stateme 9. Other	(PTO-413), te ment/Comment	ĺ
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U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04) PRIMARY EXAMINER

Application/Control Number: 10/679,179 Page 2

Art Unit: 2827

## **DETAILED ACTION**

## Allowable Subject Matter

1. Claims 1 – 20 are allowed.

2. The following is an examiner's statement of reasons for allowance:

The prior arts teach sensing circuits for a memory cell, comprising:

said memory cell having a drain capable of being connected to a first node

through a selection circuit during a read operation involving said memory cell, the

memory cell drawing a current during a read operation.

However, the prior arts fail to teach the sensing circuit, further comprising:

a first transistor being connected across the first node and a second node;

a load being connected across the second node and a third node; and

a voltage boosting circuit being coupled to a supply voltage, said supply boosting

circuit supplying a voltage at said third node which is greater than the supply voltage.

3. Any comments considered necessary by applicant must be submitted no later

than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

## Conclusion

Application/Control Number: 10/679,179

Art Unit: 2827

4. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

5. A shortened statutory period for response to this action is set to expire 3 (three)

months and 0 (zero) day from the date of this letter. Failure to respond within the period

for response will cause the application to become abandoned (see MPEP 710.02(b)).

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Ly D. Pham whose telephone number is 571-272-1793.

The examiner can normally be reached on Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor. Hoai Ho can be reached on 571-272-1777. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Ly D Pham June 28, 2005

> HOAI HO PRIMARY EXAMINER

Page 3